



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 8193

SERIAL NUMBER 10/645,900	FILING DATE 08/20/2003  RULE	CLASS 714	GROUP ART UNIT 2138	ATTORNEY DOCKET NO. 03-0862 81575
-----------------------------	---------------------------------------	--------------	------------------------	---

APPLICANTS

David Vinke, Clackamas, OR; *DG*

Ekambaram Balaji, Portland, OR;

Giuseppe Fornaciari, Milan, ITALY;

\*\* CONTINUING DATA ..... *none*

\*\* FOREIGN APPLICATIONS ..... *none*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED *DG*

\*\* 11/15/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 5	TOTAL CLAIMS 13	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance	Examiners Signature <i>DG</i>	Initials <i>DG</i>		

Verified and  
Acknowledged

ADDRESS

24319  
LSI LOGIC CORPORATION  
1621 BARBER LANE  
MS: D-106  
MILPITAS, CA  
95035

TITLE

Method and circuit for scan testing latch based random access memory

FILING FEE  RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
-----------------------------------	---	---